

DESIGN A PI CONTROLLER TO DRIVE THE STEP RESPONSE ERROR TO ZERO FOR THE UNITY FEEDBACK SYSTEM IF $G(s)=$

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IN CONTROL SYSTEM ENGINEERING, ACHIEVING ACCURATE AND STABLE SYSTEM PERFORMANCE IS PARAMOUNT. A COMMON CHALLENGE FACED BY ENGINEERS IS ENSURING THAT THE SYSTEM'S OUTPUT ACCURATELY TRACKS A DESIRED INPUT, ESPECIALLY IN THE PRESENCE OF DISTURBANCES OR MODEL UNCERTAINTIES. ONE EFFECTIVE APPROACH TO ENHANCE THE STEADY-STATE ACCURACY OF A UNITY FEEDBACK SYSTEM IS DESIGNING A PROPORTIONAL-INTEGRAL (PI) CONTROLLER. THIS ARTICLE PROVIDES A COMPREHENSIVE GUIDE ON HOW TO DESIGN A PI CONTROLLER TO DRIVE THE STEP RESPONSE ERROR TO ZERO WHEN THE PLANT TRANSFER FUNCTION IS GIVEN BY $G(s)=$, ENSURING OPTIMAL SYSTEM PERFORMANCE.

UNDERSTANDING THE UNITY FEEDBACK SYSTEM AND ITS SIGNIFICANCE

WHAT IS A UNITY FEEDBACK SYSTEM?

A UNITY FEEDBACK SYSTEM IS A CONTROL CONFIGURATION WHERE THE OUTPUT IS FED BACK DIRECTLY TO THE INPUT WITHOUT ANY ADDITIONAL SCALING. ITS PRIMARY ADVANTAGE IS SIMPLICITY, MAKING IT A COMMON CHOICE IN MANY CONTROL APPLICATIONS. THE BLOCK DIAGRAM TYPICALLY INVOLVES A PLANT $G(s)$, A CONTROLLER $C(s)$, AND A FEEDBACK PATH WITH UNITY GAIN.

IMPORTANCE OF ZERO STEADY-STATE ERROR

IN MANY PRACTICAL APPLICATIONS, HAVING ZERO STEADY-STATE ERROR TO A STEP INPUT IS CRUCIAL FOR SYSTEM ACCURACY. FOR EXAMPLE, IN POSITIONING SYSTEMS OR SPEED CONTROL, ANY PERSISTENT ERROR CAN LEAD TO PERFORMANCE DEGRADATION. ACHIEVING ZERO ERROR USUALLY DEPENDS ON THE SYSTEM TYPE AND THE CONTROLLER DESIGN.

MATHEMATICAL FOUNDATIONS OF THE CONTROL SYSTEM

SYSTEM CONFIGURATION

CONSIDER THE STANDARD UNITY FEEDBACK CONTROL SYSTEM WITH PLANT $G(s)$ AND CONTROLLER $C(s)$. THE BLOCK DIAGRAM CAN BE SUMMARIZED AS:

```
'''
Input R(s) --> [C(s)] --> [G(s)] --> Output Y(s)
||
-----
FEEDBACK (UNITY)
'''
```

THE ERROR SIGNAL, $E(s)$, IS GIVEN BY:

$$E(s) = R(s) - Y(s)$$

THE OUTPUT $Y(s)$ RELATES TO THE INPUT $R(s)$ THROUGH THE OPEN-LOOP TRANSFER FUNCTION:

$$OL(s) = C(s) G(s)$$

THE CLOSED-LOOP TRANSFER FUNCTION IS:

$$T(s) = Y(s)/R(s) = OL(s) / (1 + OL(s))$$

THE ERROR SIGNAL IN THE LAPLACE DOMAIN IS:

$$E(s) = R(s) - Y(s) = R(s) - T(s) R(s) = R(s) [1 - T(s)] = R(s) / (1 + OL(s))$$

STEADY-STATE ERROR ANALYSIS

THE FINAL VALUE THEOREM HELPS ANALYZE THE STEADY-STATE ERROR:

$$e_{ss} = \lim_{t \rightarrow \infty} e(t) = \lim_{s \rightarrow 0} s E(s)$$

FOR A STEP INPUT, $R(s) = 1/s$. THE STEADY-STATE ERROR BECOMES:

$$e_{ss} = \lim_{s \rightarrow 0} s (1/s) [1 / (1 + C(s) G(s))] = \lim_{s \rightarrow 0} 1 / (1 + C(s) G(s))$$

TO ACHIEVE ZERO STEADY-STATE ERROR FOR A STEP INPUT, THE DENOMINATOR MUST TEND TO INFINITY, WHICH IMPLIES:

$$\lim_{s \rightarrow 0} C(s) G(s) = \infty$$

THIS CONDITION IS SATISFIED IF THE SYSTEM HAS AN INTEGRATOR, I.E., $C(s)$ INCLUDES A TERM $1/s$, MAKING THE SYSTEM TYPE 1.

DESIGNING A PI CONTROLLER FOR ZERO STEADY-STATE ERROR

WHAT IS A PI CONTROLLER?

A PROPORTIONAL-INTEGRAL (PI) CONTROLLER COMBINES PROPORTIONAL CONTROL WITH INTEGRAL CONTROL. ITS TRANSFER FUNCTION IS:

$$C(s) = K_p + K_i / s$$

WHERE:

- K_p IS THE PROPORTIONAL GAIN
- K_i IS THE INTEGRAL GAIN

THE INTEGRAL COMPONENT ENSURES THE ELIMINATION OF STEADY-STATE ERROR FOR STEP INPUTS BY INTRODUCING A POLE AT THE ORIGIN, EFFECTIVELY INCREASING THE SYSTEM TYPE.

GOALS OF THE PI CONTROLLER DESIGN

- DRIVE THE STEADY-STATE ERROR TO ZERO FOR STEP INPUTS.
- MAINTAIN SYSTEM STABILITY.
- ACHIEVE ACCEPTABLE TRANSIENT RESPONSE CHARACTERISTICS SUCH AS OVERSHOOT, SETTLING TIME, AND RISE TIME.

STEP-BY-STEP DESIGN PROCEDURE

1. **IDENTIFY THE PLANT $G(s)$:** OBTAIN THE TRANSFER FUNCTION $G(s)$. FOR EXAMPLE, $G(s)$ MIGHT BE A SECOND-ORDER

SYSTEM LIKE:

$$G(s) = \frac{K}{(s + A)(s + B)}$$

OR A MORE COMPLEX TRANSFER FUNCTION DEPENDING ON THE PHYSICAL SYSTEM.

2. DETERMINE THE SYSTEM TYPE:

SINCE A PI CONTROLLER INTRODUCES AN INTEGRATOR, THE SYSTEM BECOMES TYPE 1, CAPABLE OF ZERO STEADY-STATE ERROR TO STEP INPUTS.

3. DESIGN THE CONTROLLER PARAMETERS (K_P AND K_I):

- SET THE STEADY-STATE ERROR TO ZERO: ENSURE THE OPEN-LOOP TRANSFER FUNCTION WITH THE PI CONTROLLER SATISFIES:

$$\lim_{s \rightarrow 0} s C(s) G(s) = 0$$

WHICH IS ACHIEVED BY THE INTEGRAL ACTION.

- TUNE THE PROPORTIONAL GAIN K_P : ADJUST K_P TO SHAPE THE TRANSIENT RESPONSE, SUCH AS OVERSHOOT AND SETTLING TIME.

- TUNE THE INTEGRAL GAIN K_I : ADJUST K_I TO ELIMINATE STEADY-STATE ERROR WITHOUT CAUSING EXCESSIVE OSCILLATIONS OR INSTABILITY.

A COMMON APPROACH INVOLVES INITIAL ESTIMATION BASED ON SYSTEM SPECIFICATIONS, FOLLOWED BY ITERATIVE TUNING.

4. USE ROOT LOCUS OR FREQUENCY RESPONSE METHODS:

TOOLS LIKE ROOT LOCUS, BODE PLOTS, OR NYQUIST PLOTS ASSIST IN TUNING THE CONTROLLER GAINS TO MEET STABILITY AND TRANSIENT RESPONSE CRITERIA.

5. VERIFY PERFORMANCE:

SIMULATE THE CLOSED-LOOP RESPONSE TO A STEP INPUT, CHECKING IF THE STEADY-STATE ERROR IS ZERO AND TRANSIENT SPECIFICATIONS ARE MET. ADJUST GAINS AS NECESSARY.

EXAMPLE: DESIGN OF A PI CONTROLLER FOR A GIVEN PLANT

SUPPOSE $G(s) = \frac{10}{s(s + 2)}$. THE GOAL IS TO DESIGN A PI CONTROLLER TO ELIMINATE STEADY-STATE ERROR TO A STEP INPUT.

STEP 1: IDENTIFY SYSTEM TYPE

- THE PLANT $G(s)$ HAS A POLE AT ZERO, SO IT IS TYPE 1.
- A TYPE 1 SYSTEM INHERENTLY HAS ZERO STEADY-STATE ERROR FOR STEP INPUTS.

STEP 2: BASIC CONTROLLER DESIGN

- SINCE $G(s)$ IS TYPE 1, AN INTEGRATOR IN THE CONTROLLER IS ALREADY PRESENT IMPLICITLY. BUT TO IMPROVE TRANSIENT RESPONSE AND ELIMINATE STEADY-STATE ERROR FURTHER, DESIGN A PI CONTROLLER:

$$C(s) = K_P + K_I / s$$

STEP 3: DETERMINE CONTROLLER GAINS

- USE A FREQUENCY RESPONSE METHOD OR POLE PLACEMENT TO SELECT K_P AND K_I .
- FOR SIMPLICITY, ASSUME INITIAL $K_P = 1$ AND $K_I = 1$, THEN SIMULATE THE SYSTEM RESPONSE.

STEP 4: SIMULATION AND TUNING

- RUN A SIMULATION OF THE CLOSED-LOOP SYSTEM WITH THE CHOSEN GAINS.
- ADJUST K_P AND K_I ITERATIVELY TO IMPROVE TRANSIENT RESPONSE WHILE MAINTAINING ZERO STEADY-STATE ERROR.

ADDITIONAL CONSIDERATIONS IN PI CONTROLLER DESIGN

STABILITY ANALYSIS

- ENSURE THAT THE CLOSED-LOOP SYSTEM REMAINS STABLE AFTER ADDING THE PI CONTROLLER.
- USE STABILITY CRITERIA SUCH AS ROUTH-HURWITZ, NYQUIST, OR BODE PLOTS TO VERIFY.

EFFECT OF CONTROLLER GAINS ON SYSTEM PERFORMANCE

- INCREASING K_P GENERALLY REDUCES RISE TIME BUT MAY INCREASE OVERSHOOT.
- INCREASING K_I REDUCES STEADY-STATE ERROR BUT CAN LEAD TO OSCILLATIONS OR INSTABILITY IF TOO HIGH.
- BALANCE THESE GAINS BASED ON THE DESIRED TRANSIENT AND STEADY-STATE PERFORMANCE.

ROBUSTNESS AND PRACTICAL IMPLEMENTATION

- REAL SYSTEMS HAVE UNCERTAINTIES; HENCE, ROBUSTNESS ANALYSIS IS ESSENTIAL.
- ANTI-WINDUP SCHEMES MAY BE NECESSARY TO PREVENT INTEGRATOR WINDUP.
- DIGITAL IMPLEMENTATION REQUIRES DISCRETIZATION OF THE PI CONTROLLER.

CONCLUSION

DESIGNING A PI CONTROLLER TO ENSURE ZERO STEADY-STATE ERROR FOR A UNITY FEEDBACK SYSTEM WITH A GIVEN PLANT $G(s)$ INVOLVES UNDERSTANDING THE SYSTEM DYNAMICS, LEVERAGING THE INTEGRAL ACTION TO ELIMINATE STEADY-STATE ERROR, AND CAREFULLY TUNING THE CONTROLLER PARAMETERS. BY FOLLOWING SYSTEMATIC PROCEDURES SUCH AS FREQUENCY RESPONSE ANALYSIS, ROOT LOCUS, AND SIMULATION, ENGINEERS CAN ACHIEVE A BALANCED DESIGN THAT ENSURES STABILITY, ACCURACY, AND DESIRABLE TRANSIENT RESPONSE. PROPER DESIGN OF THE PI CONTROLLER ENHANCES THE OVERALL SYSTEM PERFORMANCE, MAKING IT SUITABLE FOR A WIDE RANGE OF INDUSTRIAL AND TECHNOLOGICAL APPLICATIONS WHERE PRECISION CONTROL IS REQUIRED.

REFERENCES AND FURTHER READING

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THIS COMPREHENSIVE OVERVIEW PROVIDES A SOLID FOUNDATION FOR DESIGNING PI CONTROLLERS TO ACHIEVE ZERO STEADY-STATE ERROR IN UNITY FEEDBACK SYSTEMS, ENABLING CONTROL ENGINEERS TO OPTIMIZE SYSTEM PERFORMANCE EFFECTIVELY.

FREQUENTLY ASKED QUESTIONS

HOW DO YOU DESIGN A PI CONTROLLER TO ELIMINATE STEADY-STATE ERROR IN A UNITY FEEDBACK SYSTEM WITH TRANSFER FUNCTION $G(s)$?

TO ELIMINATE STEADY-STATE ERROR, A PI CONTROLLER INTRODUCES A ZERO TO THE SYSTEM, WHICH INCREASES THE SYSTEM TYPE AND ENSURES ZERO STEADY-STATE ERROR FOR STEP INPUTS. THE CONTROLLER PARAMETERS ARE TYPICALLY TUNED BY ANALYZING THE SYSTEM'S ERROR RESPONSE AND ADJUSTING THE PROPORTIONAL GAIN AND INTEGRAL GAIN TO ACHIEVE THE DESIRED TRANSIENT RESPONSE WHILE DRIVING THE ERROR TO ZERO.

WHAT ARE THE KEY STEPS TO DETERMINE THE PROPORTIONAL AND INTEGRAL GAINS OF A PI CONTROLLER FOR A GIVEN $G(s)$?

THE KEY STEPS INVOLVE: 1) ANALYZING THE OPEN-LOOP TRANSFER FUNCTION $G(s)$ AND THE UNITY FEEDBACK CONFIGURATION; 2) SETTING THE DESIRED STEADY-STATE ERROR TO ZERO, WHICH SUGGESTS ADDING INTEGRAL ACTION; 3) USING METHODS LIKE ROOT LOCUS, FREQUENCY RESPONSE, OR POLE PLACEMENT TO TUNE THE PI GAINS SO THAT THE CLOSED-LOOP SYSTEM'S STEP RESPONSE REACHES ZERO ERROR WITH ACCEPTABLE TRANSIENT CHARACTERISTICS.

HOW DOES THE ADDITION OF A PI CONTROLLER AFFECT THE STABILITY AND TRANSIENT RESPONSE OF THE SYSTEM?

ADDING A PI CONTROLLER INCREASES THE SYSTEM TYPE, WHICH HELPS ELIMINATE STEADY-STATE ERROR BUT CAN ALSO AFFECT STABILITY MARGINS. PROPER TUNING OF PROPORTIONAL AND INTEGRAL GAINS ENSURES THE TRANSIENT RESPONSE REMAINS WELL-BEHAVED, WITH ACCEPTABLE OVERSHOOT AND SETTLING TIME. OVERLY AGGRESSIVE GAINS MAY DESTABILIZE THE SYSTEM, SO GAINS SHOULD BE CAREFULLY ADJUSTED BASED ON STABILITY CRITERIA LIKE PHASE MARGIN AND GAIN MARGIN.

CAN YOU PROVIDE AN EXAMPLE OF DESIGNING A PI CONTROLLER FOR A SPECIFIC $G(s)$, SUCH AS $G(s) = 1 / (s + 1)$?

FOR $G(s) = 1 / (s + 1)$, A TYPICAL APPROACH IS: 1) START WITH A PROPORTIONAL GAIN TO ACHIEVE DESIRED SPEED; 2) ADD AN INTEGRAL TERM TO ELIMINATE STEADY-STATE ERROR FOR STEP INPUTS; 3) USE METHODS LIKE POLE PLACEMENT OR FREQUENCY RESPONSE TO TUNE THE PI GAINS. FOR INSTANCE, SELECTING K_p AND K_i TO SHIFT THE POLES INTO DESIRED LOCATIONS ENSURES THE STEP RESPONSE ERROR CONVERGES TO ZERO WHILE MAINTAINING SYSTEM STABILITY AND ACCEPTABLE TRANSIENT BEHAVIOR.

WHAT ARE COMMON METHODS TO VALIDATE THAT THE PI CONTROLLER DESIGN SUCCESSFULLY DRIVES THE STEP RESPONSE ERROR TO ZERO?

VALIDATION METHODS INCLUDE SIMULATION OF THE CLOSED-LOOP STEP RESPONSE USING TOOLS LIKE MATLAB/SIMULINK, ANALYZING THE ERROR OVER TIME TO CONFIRM IT CONVERGES TO ZERO, AND PERFORMING STABILITY AND ROBUSTNESS ANALYSIS SUCH AS BODE PLOTS OR ROOT LOCUS. ADDITIONALLY, EXPERIMENTAL IMPLEMENTATION AND MEASUREMENT CAN VERIFY THAT THE CONTROLLER ACHIEVES ZERO STEADY-STATE ERROR IN PRACTICE.

ADDITIONAL RESOURCES

DESIGN A PI CONTROLLER TO DRIVE THE STEP RESPONSE ERROR TO ZERO FOR THE UNITY FEEDBACK SYSTEM IF $G(s) =$

WHEN WORKING WITH CONTROL SYSTEMS, ACHIEVING ZERO STEADY-STATE ERROR FOR A STEP INPUT IS A FUNDAMENTAL GOAL. IN PARTICULAR, FOR SYSTEMS WITH A UNITY FEEDBACK CONFIGURATION, THE DESIGN OF A PROPORTIONAL-INTEGRAL (PI) CONTROLLER PLAYS A CRUCIAL ROLE IN ENSURING THAT THE SYSTEM'S STEP RESPONSE ERROR IS DRIVEN TO ZERO. THE PHRASE "DESIGN A PI CONTROLLER TO DRIVE THE STEP RESPONSE ERROR TO ZERO FOR THE UNITY FEEDBACK SYSTEM IF $G(s) =$ " ENCAPSULATES A CORE CHALLENGE IN CONTROL ENGINEERING: HOW TO SELECT AND TUNE THE CONTROLLER PARAMETERS TO MEET

STEADY-STATE ACCURACY REQUIREMENTS WHILE MAINTAINING DESIRABLE TRANSIENT RESPONSE CHARACTERISTICS.

IN THIS COMPREHENSIVE GUIDE, WE WILL EXPLORE THE PRINCIPLES BEHIND PI CONTROLLER DESIGN FOR UNITY FEEDBACK SYSTEMS, ANALYZE THE CHARACTERISTICS OF THE PLANT TRANSFER FUNCTION $G(s)$, AND WALK THROUGH PRACTICAL STEPS TO DERIVE THE CONTROLLER PARAMETERS. WHETHER YOU'RE A STUDENT, A PRACTICING ENGINEER, OR A RESEARCHER, UNDERSTANDING THIS PROCESS IS ESSENTIAL FOR DESIGNING ROBUST AND PRECISE CONTROL SYSTEMS.

UNDERSTANDING THE PROBLEM: UNITY FEEDBACK AND STEADY-STATE ERROR

BEFORE DIVING INTO THE DESIGN PROCESS, IT'S IMPORTANT TO CLARIFY THE CONTEXT AND GOALS:

- **UNITY FEEDBACK SYSTEM:** THE SYSTEM CONFIGURATION WHERE THE OUTPUT IS FED BACK DIRECTLY TO THE INPUT VIA A UNITY FEEDBACK ELEMENT. THE OVERALL TRANSFER FUNCTION IS INFLUENCED BY THE PLANT $G(s)$ AND THE CONTROLLER $C(s)$.
- **STEP RESPONSE ERROR:** WHEN A STEP INPUT IS APPLIED (E.G., A SUDDEN CHANGE FROM 0 TO 1), THE SYSTEM'S OUTPUT RESPONDS ACCORDINGLY. THE ERROR SIGNAL $e(t) = r(t) - y(t)$, WHERE $r(t)$ IS THE REFERENCE INPUT, AND $y(t)$ IS THE OUTPUT. THE GOAL IS TO MAKE $e(t)$ APPROACH ZERO AS t APPROACHES INFINITY, INDICATING PERFECT TRACKING.
- **STEADY-STATE ERROR:** THE RESIDUAL ERROR AFTER THE TRANSIENT EFFECTS HAVE DIED OUT. FOR A STEP INPUT, THE STEADY-STATE ERROR e_{ss} SHOULD BE ZERO TO ENSURE PERFECT TRACKING.

THE SIGNIFICANCE OF THE PI CONTROLLER IN CONTROL SYSTEMS

A PROPORTIONAL-INTEGRAL (PI) CONTROLLER IS A COMMON CONTROL ELEMENT USED TO ELIMINATE STEADY-STATE ERROR FOR CERTAIN TYPES OF INPUTS, ESPECIALLY STEP INPUTS. ITS TRANSFER FUNCTION IS:

$$C(s) = K_p + \frac{K_i}{s}$$

WHERE:

- K_p IS THE PROPORTIONAL GAIN.
- K_i IS THE INTEGRAL GAIN.

KEY ATTRIBUTES OF PI CONTROLLERS:

- ZERO STEADY-STATE ERROR FOR STEP INPUTS IN SYSTEMS WITH TYPE 1 PLANTS (I.E., SYSTEMS WITH A SINGLE INTEGRATOR IN OPEN-LOOP TRANSFER FUNCTION).
- IMPROVED TRANSIENT RESPONSE COMPARED TO PURE PROPORTIONAL CONTROL, ESPECIALLY IN STEADY-STATE ACCURACY.
- SIMPLICITY AND EASE OF TUNING MAKE PI CONTROLLERS WIDELY USED IN INDUSTRIAL APPLICATIONS.

STEP 1: ANALYZING THE PLANT $G(s)$

THE FIRST STEP IN DESIGNING A PI CONTROLLER IS TO UNDERSTAND THE PLANT TRANSFER FUNCTION $G(s)$. THE SPECIFIC FORM OF $G(s)$ INFLUENCES THE SYSTEM TYPE, STABILITY MARGINS, AND THE CONTROLLER PARAMETERS.

SUPPOSE $G(s)$ IS GIVEN AS A RATIONAL FUNCTION, SUCH AS:

$$G(s) = \frac{B}{s + A}$$

OR MORE GENERALLY:

$$G(s) = \frac{N(s)}{D(s)}$$

WHERE $N(s)$ AND $D(s)$ ARE POLYNOMIALS IN s .

KEY POINTS TO ANALYZE:

- SYSTEM TYPE: THE NUMBER OF INTEGRATORS IN $G(s)$. FOR ZERO STEADY-STATE ERROR TO A STEP INPUT, THE SYSTEM MUST BE TYPE 1 (I.E., HAVE ONE POLE AT THE ORIGIN OR A CONTROLLER WITH AN INTEGRATOR).
- STEADY-STATE GAIN: THE VALUE OF $G(0)$, AS IT INFLUENCES THE STEADY-STATE ERROR.
- STABILITY MARGINS: HOW THE PLANT'S POLES AND ZEROS AFFECT THE OVERALL SYSTEM STABILITY WHEN COMBINED WITH THE CONTROLLER.

STEP 2: ESTABLISHING THE CONTROL SYSTEM CONFIGURATION

THE CONTROL SYSTEM WITH A PI CONTROLLER AND UNITY FEEDBACK CAN BE REPRESENTED AS:

$$T(s) = \frac{C(s)G(s)}{1 + C(s)G(s)}$$

WHERE:

- $C(s) = K_P + \frac{K_I}{s}$.
- THE FEEDBACK IS UNITY (I.E., FEEDBACK GAIN = 1).

THE ERROR TRANSFER FUNCTION FROM THE INPUT $R(s)$ TO THE ERROR $E(s)$ IS:

$$E(s) = \frac{1}{1 + C(s)G(s)} R(s)$$

FOR A STEP INPUT $R(s) = \frac{1}{s}$, THE STEADY-STATE ERROR IS:

$$e_{ss} = \lim_{t \rightarrow \infty} e(t) = \lim_{s \rightarrow 0} s E(s)$$

STEP 3: DERIVING THE STEADY-STATE ERROR EXPRESSION

APPLYING THE FINAL VALUE THEOREM:

$$e_{ss} = \lim_{s \rightarrow 0} s E(s) = \lim_{s \rightarrow 0} s \times \frac{1}{1 + C(s)G(s)} \times \frac{1}{s} = \lim_{s \rightarrow 0} \frac{1}{1 + C(s)G(s)}$$

SINCE $C(s) = K_P + \frac{K_I}{s}$, AT $s \rightarrow 0$:

$$C(0) = K_P + \frac{K_I}{0} \rightarrow \infty$$

BECAUSE THE INTEGRAL TERM DOMINATES AS $s \rightarrow 0$, ENSURING:

$$\lim_{s \rightarrow 0} C(s) = \infty$$

THUS,

$$e_{ss} = \frac{1}{1 + \infty \times G(0)} = 0$$

PROVIDED $G(0) \neq 0$. THIS INDICATES THAT A PI CONTROLLER CAN ELIMINATE STEADY-STATE ERROR FOR A STEP INPUT IN SYSTEMS WHERE $G(0) \neq 0$, I.E., SYSTEMS WITH A NON-ZERO DC GAIN.

STEP 4: CONTROLLER PARAMETER TUNING FOR ZERO STEADY-STATE ERROR

GIVEN THE ABOVE ANALYSIS, THE MAIN GOAL IS TO SELECT (K_P) AND (K_I) SUCH THAT:

- THE SYSTEM REMAINS STABLE.
- THE STEP RESPONSE ERROR APPROACHES ZERO.

STEP 4.1: ENSURING SYSTEM TYPE 1 OR HIGHER

FOR ZERO STEADY-STATE ERROR TO A STEP INPUT, THE OPEN-LOOP TRANSFER FUNCTION MUST INCLUDE AN INTEGRATOR (I.E., A POLE AT THE ORIGIN). THIS IMPLIES:

- THE PLANT $(G(s))$ IS AT LEAST TYPE 0.
- THE PI CONTROLLER ADDS AN INTEGRATOR $(\frac{K_I}{s})$, MAKING THE OVERALL OPEN-LOOP TRANSFER FUNCTION TYPE 1.

STEP 4.2: SELECTING (K_I)

SINCE THE INTEGRAL ACTION ENSURES ZERO STEADY-STATE ERROR, THE PRIMARY CONCERN IS TUNING (K_I) TO BALANCE STEADY-STATE ACCURACY AND TRANSIENT RESPONSE.

- HIGHER (K_I) REDUCES THE STEADY-STATE ERROR AND IMPROVES ACCURACY.
- TOO HIGH (K_I) MAY CAUSE INSTABILITY OR EXCESSIVE OVERSHOOT.

STEP 4.3: SELECTING (K_P)

THE PROPORTIONAL GAIN (K_P) INFLUENCES TRANSIENT RESPONSE, STABILITY MARGINS, AND ROBUSTNESS:

- INCREASING (K_P) SPEEDS UP RESPONSE BUT CAN INDUCE OSCILLATIONS.
- PROPER TUNING INVOLVES BALANCING RESPONSE SPEED AND STABILITY.

STEP 5: PRACTICAL DESIGN PROCEDURE

HERE'S A STEP-BY-STEP PROCESS FOR DESIGNING A PI CONTROLLER TO DRIVE THE STEP RESPONSE ERROR TO ZERO:

1. ANALYZE $(G(s))$

- DETERMINE $(G(0))$, THE DC GAIN.
- VERIFY THE SYSTEM TYPE.
- IDENTIFY THE DOMINANT POLES AND ZEROS.

2. ENSURE SYSTEM IS TYPE 1 OR HIGHER

- CONFIRM THAT $(G(s))$ IS AT LEAST TYPE 0.
- THE PI CONTROLLER ADDS AN INTEGRATOR, MAKING THE OVERALL SYSTEM TYPE 1.

3. SET (K_I) FOR STEADY-STATE ERROR

- SINCE THE STEADY-STATE ERROR FOR A STEP INPUT IN A TYPE 1 SYSTEM IS ZERO WITH AN INTEGRATOR, CHOOSE (K_I) BASED ON TRANSIENT RESPONSE CONSIDERATIONS.
- START WITH A MODERATE (K_I) , E.G., BASED ON DESIRED SETTLING TIME OR DAMPING RATIO.

4. TUNE (K_P)

- USE CLASSICAL TUNING RULES (E.G., ZIEGLER-NICHOLS, COHEN-COON) OR ROOT LOCUS METHODS.

- ADJUST (K_P) TO IMPROVE TRANSIENT RESPONSE WHILE MAINTAINING STABILITY.

5. VALIDATE VIA SIMULATION

- SIMULATE THE STEP RESPONSE OF THE CLOSED-LOOP SYSTEM.
- CHECK IF THE ERROR APPROACHES ZERO AND THE RESPONSE MEETS PERFORMANCE SPECIFICATIONS.

6. ITERATE AND REFINE

- FINE-TUNE (K_P) AND (K_I) BASED ON SIMULATION RESULTS.
- AIM FOR MINIMAL OVERSHOOT, ACCEPTABLE SETTLING TIME, AND ZERO STEADY-STATE ERROR.

ADDITIONAL CONSIDERATIONS

- ROBUSTNESS: ENSURE THE CONTROLLER PERFORMS WELL UNDER MODEL UNCERTAINTIES.
- LIMITATIONS: INTEGRAL ACTION CAN CAUSE WINDUP; ANTI-WINDUP SCHEMES MAY BE NECESSARY.
- NON-IDEALITIES: REAL SYSTEMS MAY HAVE ACTUATOR SATURATION, NOISE, OR NONLINEARITIES THAT AFFECT CONTROLLER PERFORMANCE.

SUMMARY: KEY TAKEAWAYS

- A PI CONTROLLER IS EFFECTIVE FOR ELIMINATING STEADY-STATE ERROR TO A

Design A Pi Controller To Drive The Step Response Error To Zero For The Unity Feedback System If G S

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