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Understanding the differences in input impedance between a Common Emitter (CE) configuration and a Common Base (CB) configuration is crucial for designing and analyzing transistor amplifier circuits. When both transistors operate under identical conditions, the input impedance of the CE typically appears significantly lower than that of the CB. This disparity influences how each configuration interacts with the input signal source, affects overall circuit stability, and determines the suitability for specific applications. This article explores the fundamental reasons behind this difference, providing insights into the underlying physics, circuit behavior, and practical implications.

Fundamental Concepts of Transistor Configurations

What Is a Common Emitter (CE) Configuration?

The Common Emitter configuration is one of the most widely used transistor amplifier arrangements. In this setup, the emitter terminal is common to both the input and output circuits. It typically offers high voltage and current gain, making it suitable for amplification purposes. The input signal is applied between the base and emitter, while the output is taken between the collector and emitter.

What Is a Common Base (CB) Configuration?

In contrast, the Common Base configuration has the base terminal grounded or held at a constant potential. The input signal is applied between the emitter and base, and the output is taken from the collector and base. The CB configuration is known for its low input impedance and high bandwidth, making it ideal for high-frequency applications.

Input Impedance: Definition and Significance

Input impedance (Z_{in}) of a transistor circuit is the measure of how much the circuit resists or allows the flow of input current when a voltage is applied. It is a crucial parameter because it determines how the circuit interacts with the source signal. A high input impedance means the circuit draws minimal current from the source, preserving the integrity of the input signal. Conversely, a low input impedance can load the source, potentially distorting the input signal and reducing overall efficiency.

Understanding the differences in input impedance between CE and CB configurations helps engineers design circuits that optimize signal transfer, minimize losses, and meet specific application requirements.

Why Does The CE Have Much Lower Input Impedance Than The CB?

1. Transistor Internal Structure and Operating Principles

The fundamental reason behind the lower input impedance of the CE configuration lies in the transistor's internal operation. In the BJT (Bipolar Junction Transistor), the current flow between the collector and emitter is controlled by the base-emitter junction. The input impedance seen at the base terminal depends on how the base-emitter junction behaves under forward bias.

In the CE configuration, the input is applied between the base and emitter. Since the base-emitter junction is forward-biased, it behaves like a diode with a low dynamic resistance (r_{be}). This dynamic resistance is given approximately by:

- $r_{be} \approx 25\text{mV} / I_B$

where I_B is the base current. Because of this diode-like behavior, the input impedance seen at the base in a CE stage tends to be low, typically in the range of a few hundred ohms to a few kilo-ohms.

In contrast, in the CB configuration, the input is applied between the emitter and base, but with the base held at a fixed potential. The input impedance here is primarily determined by the emitter-base junction's behavior, which appears as a high impedance because the base is at a fixed potential, and the emitter acts as the input terminal. The collector-base junction's influence is minimal in this configuration.

2. Effect of Biasing and Circuit Configuration

Biasing methods influence the impedance characteristics significantly. In CE configuration, the biasing ensures the base-emitter junction remains forward-biased, maintaining the low dynamic resistance r_{be} . Because the input current must pass through this diode-like junction, the impedance remains low.

In the CB configuration, biasing is set such that the base is common and at a fixed potential, often at ground or a bias voltage. The emitter is then the input terminal. The input impedance here is mainly determined by the emitter's dynamic resistance (r_e) multiplied by the current gain (β), leading to a much higher impedance.

3. Mathematical Perspective: Small-Signal Model Analysis

Analyzing the small-signal equivalent circuits provides a quantitative understanding of impedance differences:

- For CE configuration, the input impedance ($Z_{in,CE}$) is approximately:

$$\bullet Z_{in,CE} \approx r_{be} \parallel (\beta + 1) R_E$$

where R_E is the emitter resistor, and r_{be} is the diode resistance at the base-emitter junction.

- For CB configuration, the input impedance ($Z_{in,CB}$) is primarily:

$$\bullet Z_{in,CB} \approx r_e = 25mV / I_E$$

which is typically much higher because r_e is usually in the range of a few ohms to tens of ohms, but when considering the overall circuit, the impedance appears significantly larger compared to CE.

Practical Implications of Impedance Differences

1. Signal Source Compatibility

A low input impedance in CE configurations means that the circuit can load the signal source more heavily. This is advantageous when the source has a high output impedance, but it can cause signal attenuation. Conversely, the high input impedance of CB amplifiers makes them suitable for high-impedance sources like sensors or microphones, ensuring minimal signal loss.

2. Frequency Response and Bandwidth

Input impedance influences the frequency response of transistor amplifiers. The lower the impedance, the more susceptible the circuit is to signal attenuation at higher frequencies due to parasitic capacitances. The CE configuration's lower input impedance can limit its high-frequency performance unless carefully compensated, while the CB's high impedance allows for broader bandwidth applications.

3. Circuit Design Considerations

Designers choose the configuration based on impedance requirements:

- **CE Amplifiers:** Preferable for voltage amplification where low input impedance is acceptable or desired.
- **CB Amplifiers:** Ideal for impedance matching with high-impedance sources and high-frequency applications.

Summary and Conclusion

The disparity in input impedance between the Common Emitter and Common Base configurations stems from their internal structure, biasing, and signal path characteristics. The CE configuration exhibits a much lower input impedance primarily because the base-emitter junction acts like a diode with a low dynamic resistance, and the input signal is applied directly across it. Conversely, the CB configuration, with its fixed base potential and different current pathways, presents a substantially higher input impedance.

Understanding these differences is vital for circuit designers aiming to optimize amplifier performance, ensure proper impedance matching, and achieve desired frequency responses. Whether designing audio amplifiers, RF circuits, or sensor interfaces, selecting the appropriate configuration based on input impedance considerations ensures efficient, reliable, and high-quality circuit operation.

In conclusion, the input impedance of the CE is much lower than that of a CB under the same operating conditions due to the intrinsic properties of the transistor's junctions and the way the circuit is biased and configured. Recognizing this fundamental difference helps in making informed choices in electronic circuit design and application.

Frequently Asked Questions

Why does the common emitter (CE) configuration have a lower input impedance compared to the common base (CB) configuration under the same operating conditions?

In a common emitter configuration, the input is applied between the base and emitter, where the input impedance is influenced by the transistor's current gain and the base-emitter junction. This results in a lower input impedance because the base-emitter junction behaves like a forward-biased diode, allowing more current flow. Conversely, in a common base configuration, the input is at the emitter, and the input impedance is primarily determined by the emitter resistor and the transistor's internal parameters, leading to a higher impedance.

How does the transistor's current gain affect the input impedance in CE and CB configurations?

The current gain (β) amplifies the input signal in a common emitter configuration, effectively reducing the input impedance because a small base current controls a larger collector current. In contrast, the common base configuration does not amplify current at the input in the same way, resulting in a higher input impedance since the input is at the emitter where the current gain influence is different.

Can the input impedance of a CE amplifier be increased to match that of a CB amplifier? If so, how?

Yes, the input impedance of a CE amplifier can be increased by adding impedance-matching components such as emitter degeneration resistors or using biasing techniques that increase the effective input resistance. Using a large emitter resistor, for example, can raise the input impedance by providing negative feedback, making it comparable to the input impedance of a CB configuration.

What implications does the lower input impedance of a CE amplifier have on circuit design and signal sourcing?

A lower input impedance in a CE amplifier means it draws more current from the source, which can lead to signal attenuation or loading effects if the source impedance is high. Therefore, it is important to match or buffer the source with a suitable impedance or use impedance matching techniques to prevent signal degradation and ensure proper operation.

Why is the common emitter configuration more commonly used in voltage amplification despite its lower input impedance?

The common emitter configuration provides significant voltage and current gain, making it highly effective for amplification purposes. Although it has a lower input impedance, its ability to amplify signals effectively outweighs this drawback, especially when input impedance matching is managed with appropriate circuitry. Its versatility and high gain make it a popular choice in many applications.

How do operating conditions influence the difference in input impedance between CE and CB configurations?

Operating conditions such as bias point, transistor parameters, and load resistances affect the input impedance in both configurations. Under the same conditions, the CE configuration inherently exhibits a lower input impedance due to its circuit topology and the behavior of the base-emitter junction, whereas the CB configuration maintains a higher impedance as its input is at the emitter. Changes in biasing or load can modify these impedances but the fundamental difference remains.

Additional Resources

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In the realm of electronic circuits, particularly in the design of transistor amplifiers, understanding the input impedance is crucial for optimizing performance and ensuring compatibility with other circuit components. Among the common configurations—Common Emitter (CE) and Common Base (CB)—a notable difference emerges: the input impedance of the CE configuration is significantly lower than that of the CB when operating under identical conditions. This disparity has profound implications for circuit design, signal transfer, and overall system efficiency. This article delves into the fundamental reasons behind this difference, explores the underlying principles, and discusses the practical consequences for engineers and enthusiasts alike.

Understanding Transistor Configurations: CE vs. CB

Before examining the impedance characteristics, it's essential to comprehend the basic structures and operation modes of the Common Emitter and Common Base configurations.

The Common Emitter (CE) Configuration

The CE configuration is perhaps the most versatile and widely used transistor setup in amplification circuits. In this arrangement:

- The emitter terminal is common to both input and output circuits.
- The input signal is applied between the base and emitter.
- The output is taken between the collector and emitter.

This configuration offers high voltage and current gain, making it suitable for various amplification tasks. Its impedance characteristics are influenced heavily by the transistor's internal parameters and the biasing circuitry.

The Common Base (CB) Configuration

In the CB configuration:

- The base terminal is common to both input and output circuits.
- The input signal is applied between the emitter and base.
- The output is taken from the collector relative to the base.

This setup provides low input impedance and high output impedance, which makes it suitable for high-frequency applications. Its behavior differs markedly from the CE configuration, especially in terms of impedance and gain.

Fundamental Reasons for the Impedance Difference

The core of the impedance disparity lies in the intrinsic properties of the transistor's current flow and the way input signals are coupled into the device in each configuration.

The Role of the Transistor's Current Components

A bipolar junction transistor (BJT) operates through the movement of charge carriers, with the collector current (I_C) primarily controlled by the base current (I_B) and, in the case of a common-base configuration, by the emitter current (I_E). The relationships between these currents and voltages underpin the impedance characteristics.

Input Impedance in the Common Emitter (CE) Configuration

In the CE configuration:

- The input is applied between the base and emitter.
- The emitter is often bypassed or connected to ground through an emitter resistor (R_e).
- The impedance observed at the base includes the transistor's internal parameters and the biasing network.

Key Factors:

- The input is effectively a voltage-controlled current source (V_{BB} controlling I_B).
- The input impedance is influenced by the current gain (β or h_{FE}) and the transistor's internal resistance.

Mathematically, the input impedance ($Z_{in,CE}$) can be approximated as:

$$Z_{in,CE} \approx (\beta + 1) (r_e + R_e)$$

where:

- r_e is the intrinsic emitter resistance, typically small ($\sim 25 \text{ m}\Omega$ at room temperature).
- R_e is the external emitter resistor.

Because of the multiplication by $(\beta + 1)$, the impedance seen from the base in a CE configuration tends to be higher than that from the emitter, but still moderate overall.

Input Impedance in the Common Base (CB) Configuration

In the CB configuration:

- The input is applied between the emitter and base.
- The base is common to both input and output.
- The input impedance is primarily determined by the transistor's emitter resistance and the intrinsic emitter resistance.

The approximate input impedance ($Z_{in,CB}$) is given by:

$$Z_{in,CB} \approx r_e$$

which is inherently low, typically around 25-100 Ω , depending on biasing and temperature.

Why is it so low?

Because in the CB configuration:

- The input signal modulates the emitter current directly.
- The base terminal is at a fixed potential (grounded or biased), and the emitter current is proportional to the emitter voltage divided by r_e .
- The transistor acts like a low-impedance current-controlled emitter resistor, leading to a low input impedance.

Deep Dive: The Physics Behind the Impedance Disparity

The core physical reason for the difference in input impedance hinges on how the transistor's internal charge carrier dynamics interact with the applied signals.

Charge Control and Internal Resistance

- In the CE configuration, the base-emitter junction acts as a voltage-controlled current source. The input current (base current) is relatively small, and the circuit "sees" a higher impedance because it effectively encounters the current gain magnified by $(\beta + 1)$.
- In contrast, the CB configuration sees the emitter as the input terminal, directly controlling the emitter current with minimal internal voltage drop. The emitter current is primarily controlled by the emitter-to-base voltage and the intrinsic emitter resistance (r_e), which is very low, resulting in a naturally low input impedance.

Impact of Internal Transistor Parameters

The transistor's current gain (β) influences the input impedance in the CE configuration significantly, as the base current is a small fraction of the collector current, but the impedance appears magnified due to the transistor's current amplification. Conversely, in the CB configuration, the current is directly controlled by the emitter voltage and the intrinsic resistance, with less amplification effect on impedance.

Practical Implications: Why Does This Matter?

Understanding the impedance characteristics of CE and CB configurations is not merely academic; it has tangible consequences in real-world circuit design.

Signal Compatibility and Loading Effects

- CE Configuration:

The higher input impedance means that the amplifier draws less current from the source. This is advantageous when interfacing with high-impedance sources like microphones or sensors, minimizing signal loading and distortion.

- CB Configuration:

The low input impedance can load down high-impedance sources, potentially reducing signal levels. However, its low input impedance makes it suitable for high-frequency

applications where low input impedance helps match transmission line characteristics.

Frequency Response and Stability

- The low input impedance of CB configurations can lead to better high-frequency response because parasitic capacitances are less significant when the impedance is low.
- Conversely, the higher impedance of CE amplifiers can introduce parasitic effects at high frequencies, necessitating careful design considerations.

Amplifier Design Choices

Engineers select configurations based on impedance requirements:

- For impedance matching with high-impedance sources, CE or common collector (emitter follower) configurations are preferred.
- For low-impedance input applications, CB configurations are often employed, especially in RF circuits.

Summarizing the Key Differences

Aspect	Common Emitter (CE)	Common Base (CB)
Input Terminal	Base	Emitter
Input Impedance	Moderate to high, $(\beta + 1) r_e + R_e$	Very low ($\sim r_e$)
Signal Control	Voltage-controlled current source	Voltage-controlled emitter current
Amplification Effect on Impedance	Magnifies input impedance	Maintains low input impedance
Typical Applications	General amplification, audio	High-frequency RF circuits

Conclusion: The Significance of Impedance Variations

The marked difference in input impedance between the CE and CB transistor configurations underpins many practical design decisions in electronics. The low input impedance of the CB configuration stems from its direct control of emitter current through the emitter terminal, coupled with the intrinsic emitter resistance. Meanwhile, the CE configuration’s higher impedance arises from the transistor’s current gain and the associated internal parameters.

For engineers and circuit designers, recognizing these intrinsic properties allows for better matching of circuit components, improved signal integrity, and optimized performance across various applications. Whether designing high-fidelity audio amplifiers, RF transceivers, or sensor interfaces, understanding the fundamental behavior of transistors in different configurations remains a cornerstone of effective electronic design.

In summary, the input impedance of the CE being much lower than that of a CB under the same conditions is rooted in the transistor’s internal physics and circuit topology. This distinction is not only a matter of theoretical interest but a practical tool in crafting the next

generation of electronic devices.

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