

IN THE FOLLOWING CODE SEQUENCE, SHOW THE VALUE OF AL AFTER EACH SHIFT OR ROTATE INSTRUCTION HAS EXECUTED:

IN THE FOLLOWING CODE SEQUENCE, SHOW THE VALUE OF AL AFTER EACH SHIFT OR ROTATE INSTRUCTION HAS EXECUTED:

UNDERSTANDING HOW SHIFT AND ROTATE INSTRUCTIONS MODIFY THE AL REGISTER IN ASSEMBLY LANGUAGE IS FUNDAMENTAL FOR GRASPING LOW-LEVEL DATA MANIPULATION. THESE INSTRUCTIONS ARE PIVOTAL IN VARIOUS PROGRAMMING SCENARIOS, INCLUDING BITWISE OPERATIONS, DATA ENCRYPTION, COMPRESSION ALGORITHMS, AND EMBEDDED SYSTEMS PROGRAMMING. THIS ARTICLE EXPLORES A SPECIFIC CODE SEQUENCE INVOLVING SHIFT AND ROTATE INSTRUCTIONS, METICULOUSLY DEMONSTRATING THE VALUE OF THE AL REGISTER AFTER EACH OPERATION. BY ANALYZING EACH STEP, READERS WILL GAIN A THOROUGH UNDERSTANDING OF HOW THESE INSTRUCTIONS INFLUENCE REGISTER CONTENTS AND HOW TO INTERPRET THEIR EFFECTS IN ASSEMBLY LANGUAGE PROGRAMMING.

OVERVIEW OF SHIFT AND ROTATE INSTRUCTIONS

BEFORE DIVING INTO THE SPECIFIC CODE SEQUENCE, IT'S ESSENTIAL TO UNDERSTAND THE BASIC TYPES OF SHIFT AND ROTATE INSTRUCTIONS, THEIR SYNTAX, AND THEIR TYPICAL USES.

TYPES OF SHIFT INSTRUCTIONS

SHIFT INSTRUCTIONS MOVE BITS IN A REGISTER OR MEMORY LOCATION TO THE LEFT OR RIGHT, EFFECTIVELY MULTIPLYING OR DIVIDING THE VALUE BY POWERS OF TWO. THE MOST COMMON SHIFT INSTRUCTIONS INCLUDE:

- SHL (SHIFT LOGICAL LEFT): SHIFTS BITS TO THE LEFT, FILLING WITH ZEROS.
- SHR (SHIFT LOGICAL RIGHT): SHIFTS BITS TO THE RIGHT, FILLING WITH ZEROS.
- SAR (SHIFT ARITHMETIC RIGHT): SHIFTS BITS TO THE RIGHT, PRESERVING THE SIGN BIT FOR SIGNED NUMBERS.

TYPES OF ROTATE INSTRUCTIONS

ROTATE INSTRUCTIONS MOVE BITS AROUND THE ENDS OF THE REGISTER, ROTATING THE BITS IN A CIRCULAR FASHION:

- ROL (ROTATE LEFT): ROTATES BITS TO THE LEFT; THE LEFTMOST BIT WRAPS AROUND TO THE RIGHTMOST POSITION.
- ROR (ROTATE RIGHT): ROTATES BITS TO THE RIGHT; THE RIGHTMOST BIT WRAPS AROUND TO THE LEFTMOST POSITION.
- RCL (ROTATE THROUGH CARRY LEFT): ROTATES BITS TO THE LEFT THROUGH THE CARRY FLAG.
- RCR (ROTATE THROUGH CARRY RIGHT): ROTATES BITS TO THE RIGHT THROUGH THE CARRY FLAG.

THESE INSTRUCTIONS ARE ESSENTIAL FOR MANIPULATING INDIVIDUAL BITS, SETTING OR CLEARING SPECIFIC FLAGS, AND IMPLEMENTING COMPLEX BITWISE ALGORITHMS.

EXAMPLE CODE SEQUENCE AND ITS ANALYSIS

SUPPOSE WE HAVE THE FOLLOWING ASSEMBLY CODE SEQUENCE, WHERE THE INITIAL VALUE OF AL IS SET TO 0xA5 (BINARY 10100101). WE WILL ANALYZE THE VALUE OF AL AFTER EACH INSTRUCTION.

```
"" ASSEMBLY
MOV AL, 0xA5 ; AL = 0xA5 (BINARY 10100101)
```

```
SHL AL, 1 ; SHIFT AL LEFT BY 1
SHR AL, 2 ; SHIFT AL RIGHT BY 2
ROL AL, 1 ; ROTATE AL LEFT BY 1
ROR AL, 3 ; ROTATE AL RIGHT BY 3
'''
```

TO UNDERSTAND THE EFFECT OF EACH INSTRUCTION, LET'S EXAMINE EACH STEP CAREFULLY.

STEP-BY-STEP ANALYSIS OF THE CODE SEQUENCE

INITIAL STATE

- AL = 0xA5
- BINARY REPRESENTATION: 10100101
- HEXADECIMAL: 0xA5

AFTER 'SHL AL, 1' (SHIFT LOGICAL LEFT BY 1)

- OPERATION: AL = AL << BINARY BEFORE SHIFT: 10100101
- SHIFT LEFT BY 1: 01001010
- THE LEFTMOST BIT (1) IS SHIFTED OUT AND LOST.
- THE RIGHTMOST BIT IS FILLED WITH 0.
- AL = 0x4A

VALUE OF AL AFTER 'SHL AL, 1': 0x4A

AFTER 'SHR AL, 2' (SHIFT LOGICAL RIGHT BY 2)

- OPERATION: AL = AL >> 2
- BINARY BEFORE SHIFT: 01001010
- SHIFT RIGHT BY 2: 00010010
- THE TWO RIGHTMOST BITS (10) ARE SHIFTED OUT.
- THE LEFTMOST BITS ARE FILLED WITH ZEROS.
- AL = 0x12

VALUE OF AL AFTER 'SHR AL, 2': 0x12

AFTER 'ROL AL, 1' (ROTATE LEFT BY 1)

- OPERATION: AL = ROL AL, 1
- BINARY BEFORE ROTATE: 00010010
- THE LEFTMOST BIT (0) WRAPS AROUND TO THE RIGHTMOST POSITION.
- ROTATION:
- LEFT SHIFT: 00100100

- THE LEFTMOST BIT (0) MOVES TO THE RIGHTMOST POSITION.
- AL = 0x24

VALUE OF AL AFTER `ROL AL, 1`: 0x24

AFTER `ROR AL, 3` (ROTATE RIGHT BY 3)

- OPERATION: AL = ROR AL, 3
- BINARY BEFORE ROTATE: 00100100
- ROTATE RIGHT BY 3:
 1. MOVE BITS 3 POSITIONS TO THE RIGHT:
 - BITS SHIFTED OUT: LAST 3 BITS (100)
 - REMAINING BITS: 00100
 2. THE BITS SHIFTED OUT (100) WRAP AROUND TO THE LEFT:
 - NEW BINARY: 10000100
 - AL = 0x84

FINAL VALUE OF AL AFTER `ROR AL, 3`: 0x84

SUMMARY OF AL VALUES AFTER EACH INSTRUCTION

INSTRUCTION	AL VALUE (Hex)	AL VALUE (BINARY)	EXPLANATION
INITIAL (MOV AL, 0xA5)	0xA5	10100101	INITIAL VALUE
`SHL AL, 1`	0x4A	01001010	SHIFTED LEFT, MSB LOST, LSB FILLED WITH 0
`SHR AL, 2`	0x12	00010010	SHIFTED RIGHT TWICE, LSBs LOST
`ROL AL, 1`	0x24	00100100	ROTATED LEFT, MSB WRAPPED AROUND
`ROR AL, 3`	0x84	10000100	ROTATED RIGHT THrice, BITS WRAPPED AROUND

THIS STEP-BY-STEP BREAKDOWN DEMONSTRATES HOW EACH SHIFT OR ROTATE IMPACTS THE REGISTER'S CONTENT, PROVIDING A CLEAR UNDERSTANDING OF BITWISE DATA MANIPULATION AT THE HARDWARE LEVEL.

PRACTICAL APPLICATIONS OF SHIFT AND ROTATE INSTRUCTIONS

UNDERSTANDING THE EFFECTS OF SHIFT AND ROTATE INSTRUCTIONS IS ESSENTIAL IN MULTIPLE DOMAINS:

DATA ENCRYPTION AND DECRYPTION

- ROTATIONS ARE OFTEN USED IN CRYPTOGRAPHIC ALGORITHMS LIKE DES AND AES FOR PERMUTATION AND DIFFUSION.
- SHIFTS ARE USED IN SIMPLE ENCRYPTION SCHEMES, SUCH AS CAESAR CIPHERS OR BIT MASKING.

BIT MASKING AND FLAG OPERATIONS

- SHIFTS HELP ISOLATE OR MODIFY SPECIFIC BITS WITHIN A REGISTER.

- ROTATE THROUGH CARRY INSTRUCTIONS FACILITATE MULTI-BYTE DATA OPERATIONS OR COMPLEX FLAG MANIPULATIONS.

EMBEDDED SYSTEMS AND DEVICE CONTROL

- HARDWARE REGISTERS CONTROLLING PERIPHERALS OFTEN REQUIRE PRECISE BIT MODIFICATIONS.
- SHIFTS AND ROTATIONS ENABLE SETTING, CLEARING, OR TOGGING SPECIFIC BITS EFFICIENTLY.

PERFORMANCE OPTIMIZATION

- SHIFT AND ROTATE INSTRUCTIONS ARE HARDWARE-OPTIMIZED FOR SPEED, MAKING THEM PREFERABLE IN PERFORMANCE-CRITICAL CODE.

CONCLUSION

MASTERING SHIFT AND ROTATE INSTRUCTIONS, ALONG WITH UNDERSTANDING THEIR IMPACT ON REGISTER VALUES, IS A FUNDAMENTAL SKILL IN ASSEMBLY LANGUAGE PROGRAMMING AND LOW-LEVEL SYSTEM DESIGN. BY ANALYZING EACH INSTRUCTION'S EFFECT, AS DEMONSTRATED IN THE EXAMPLE SEQUENCE, PROGRAMMERS CAN WRITE MORE EFFICIENT, PRECISE, AND EFFECTIVE CODE. RECOGNIZING HOW BITS MOVE AND ROTATE WITHIN REGISTERS ENABLES BETTER CONTROL OVER HARDWARE AND FACILITATES THE IMPLEMENTATION OF COMPLEX ALGORITHMS, CRYPTOGRAPHIC ROUTINES, AND DEVICE MANAGEMENT TASKS. AS YOU CONTINUE EXPLORING ASSEMBLY LANGUAGE, KEEP PRACTICING WITH DIFFERENT SEQUENCES TO DEEPEN YOUR UNDERSTANDING OF THESE POWERFUL INSTRUCTIONS.

FREQUENTLY ASKED QUESTIONS

IN THE GIVEN CODE SEQUENCE, HOW DOES THE VALUE OF AL CHANGE AFTER A 'SHL AL, 1' INSTRUCTION EXECUTES?

AFTER 'SHL AL, 1', AL IS SHIFTED LEFT BY ONE BIT, EFFECTIVELY DOUBLING ITS VALUE; THE MOST SIGNIFICANT BIT IS SHIFTED INTO THE CARRY FLAG, AND AL'S BITS ARE SHIFTED LEFT, WITH ZERO FILLED INTO THE LEAST SIGNIFICANT BIT.

WHAT IS THE VALUE OF AL AFTER EXECUTING 'ROR AL, 1' IF AL INITIALLY CONTAINS 0x85?

INITIALLY, AL = 0x85 (10000101). AFTER 'ROR AL, 1', AL BECOMES 0xC2 (11000010), AS THE BITS ARE ROTATED RIGHT, WITH THE LEAST SIGNIFICANT BIT MOVING TO THE MOST SIGNIFICANT POSITION.

HOW DOES THE 'SHR AL, 1' INSTRUCTION AFFECT THE VALUE OF AL IF AL INITIALLY HOLDS 0x03?

EXECUTING 'SHR AL, 1' SHIFTS AL RIGHT BY ONE BIT; WITH AL=0x03 (00000011), IT BECOMES 0x01 (00000001), AND THE LEAST SIGNIFICANT BIT SHIFTED OUT AFFECTS THE CARRY FLAG.

AFTER A 'ROL AL, 2' INSTRUCTION, HOW DOES THE VALUE OF AL CHANGE IF AL INITIALLY CONTAINS 0x81?

STARTING WITH AL=0x81 (10000001), 'ROL AL, 2' ROTATES AL LEFT BY 2 BITS; THE BITS SHIFTED OUT FROM THE

LEFT RE-ENTER FROM THE RIGHT, RESULTING IN AL=0x03 (00000011).

WHAT IS THE EFFECT ON AL AFTER EXECUTING 'SHL AL, 3' IF AL INITIALLY CONTAINS 0x10?

WITH AL=0x10 (00010000), 'SHL AL, 3' SHIFTS BITS LEFT BY 3 POSITIONS, RESULTING IN AL=0x80 (10000000), AND THE BITS SHIFTED OUT ARE STORED IN THE CARRY FLAG.

IF AL INITIALLY CONTAINS 0xFF, WHAT IS ITS VALUE AFTER 'SHR AL, 4' EXECUTES?

AFTER 'SHR AL, 4', AL BECOMES 0x0F (00001111), AS THE BITS ARE SHIFTED RIGHT BY FOUR POSITIONS, AND THE BITS SHIFTED OUT FROM THE RIGHT ARE DISCARDED.

EXPLAIN HOW THE VALUE OF AL CHANGES AFTER 'RCL AL, 1' IF AL INITIALLY IS 0x7F AND CARRY FLAG IS SET.

IN 'RCL AL, 1', AL IS ROTATED THROUGH THE CARRY FLAG. IF AL=0x7F (01111111) AND CARRY=1, AFTER EXECUTION, AL BECOMES 0xFF (11111111), AND THE CARRY FLAG REMAINS SET.

IN A CODE SEQUENCE, HOW CAN YOU DETERMINE AL'S VALUE AFTER MULTIPLE SHIFTS OR ROTATES?

YOU NEED TO ANALYZE EACH INSTRUCTION STEP-BY-STEP, CONSIDERING THE INITIAL VALUE OF AL, THE TYPE OF SHIFT OR ROTATE, THE NUMBER OF BITS SHIFTED, AND HOW EACH OPERATION AFFECTS THE BITS AND FLAGS. SIMULATING EACH INSTRUCTION SEQUENTIALLY HELPS DETERMINE AL'S FINAL VALUE.

ADDITIONAL RESOURCES

IN THE FOLLOWING CODE SEQUENCE, SHOW THE VALUE OF AL AFTER EACH SHIFT OR ROTATE INSTRUCTION HAS EXECUTED:
AN IN-DEPTH EXAMINATION OF REGISTER OPERATIONS IN ASSEMBLY LANGUAGE

INTRODUCTION

ASSEMBLY LANGUAGE PROGRAMMING OFTEN INVOLVES MANIPULATING DATA AT THE BIT AND BYTE LEVELS, PROVIDING FINE-GRAINED CONTROL OVER HARDWARE OPERATIONS. ONE FUNDAMENTAL ASPECT OF SUCH MANIPULATION IS THE USE OF SHIFT AND ROTATE INSTRUCTIONS, WHICH MODIFY THE BITS WITHIN REGISTERS TO ACHIEVE SPECIFIC OUTCOMES—BE IT DATA ENCRYPTION, CHECKSUM CALCULATIONS, OR HARDWARE COMMUNICATION PROTOCOLS.

A COMMON EDUCATIONAL EXERCISE INVOLVES OBSERVING HOW THE VALUE OF A PARTICULAR REGISTER, SUCH AS AL IN THE x86 ARCHITECTURE, EVOLVES AFTER EACH SHIFT OR ROTATE INSTRUCTION IS EXECUTED. THIS PROCESS OFFERS INVALUABLE INSIGHT INTO THE INNER WORKINGS OF LOW-LEVEL DATA TRANSFORMATION, ENABLING PROGRAMMERS AND STUDENTS TO UNDERSTAND THE IMPLICATIONS OF EACH INSTRUCTION ON THE REGISTER'S CONTENT.

THIS ARTICLE OFFERS A COMPREHENSIVE, DETAILED ANALYSIS OF A TYPICAL CODE SEQUENCE THAT EMPLOYS SHIFT AND ROTATE INSTRUCTIONS ON THE AL REGISTER. WE EXPLORE THE IMPORTANCE OF UNDERSTANDING REGISTER STATES AFTER EACH OPERATION, DISSECT THE CODE STEP-BY-STEP, AND HIGHLIGHT THE SIGNIFICANCE OF THESE INSTRUCTIONS IN REAL-WORLD APPLICATIONS. WHETHER YOU'RE A STUDENT LEARNING ASSEMBLY LANGUAGE, A DEVELOPER DEBUGGING HARDWARE DRIVERS, OR AN ENTHUSIAST INTERESTED IN LOW-LEVEL PROGRAMMING, THIS REVIEW AIMS TO DEEPEN YOUR UNDERSTANDING OF THESE FUNDAMENTAL OPERATIONS.

BACKGROUND: REGISTERS, AL, AND BITWISE OPERATIONS

WHAT IS AL?

IN THE X86 ARCHITECTURE, THE REGISTER AL IS AN 8-BIT REGISTER THAT FORMS PART OF THE AX REGISTER (WHICH IS 16 BITS). AL IS OFTEN USED FOR BYTE-LEVEL DATA OPERATIONS, SUCH AS PROCESSING CHARACTERS, SMALL INTEGERS, OR AS TEMPORARY STORAGE DURING COMPLEX INSTRUCTIONS.

SIGNIFICANCE OF SHIFT AND ROTATE INSTRUCTIONS

SHIFT AND ROTATE INSTRUCTIONS ARE USED TO MANIPULATE THE BITS WITHIN A REGISTER:

- SHIFT INSTRUCTIONS: MOVE BITS LEFT OR RIGHT, FILLING THE VACATED POSITIONS WITH ZEROS (LOGICAL SHIFT) OR THE SIGN BIT (ARITHMETIC SHIFT).
- ROTATE INSTRUCTIONS: ROTATE BITS AROUND THE REGISTER'S BOUNDARY, EITHER THROUGH THE CARRY FLAG (CIRCULAR ROTATION) OR WITHIN THE REGISTER ITSELF.

THESE OPERATIONS ARE INTEGRAL IN ALGORITHMS INVOLVING BIT MASKING, DATA ENCODING, CRYPTOGRAPHY, AND HARDWARE CONTROL.

THE CODE SEQUENCE: AN EXAMPLE WALKTHROUGH

SUPPOSE WE HAVE THE FOLLOWING ASSEMBLY CODE SEQUENCE:

```
""ASSEMBLY
MOV AL, 0x95 ; LOAD AL WITH HEXADECIMAL 0x95 (BINARY 1001 0101)
SHL AL, 1 ; SHIFT AL LEFT BY 1
ROR AL, 2 ; ROTATE AL RIGHT BY 2
SHR AL, 3 ; SHIFT AL RIGHT BY 3
ROL AL, 4 ; ROTATE AL LEFT BY 4
""
```

THIS SEQUENCE IS REPRESENTATIVE OF TYPICAL SHIFT AND ROTATE OPERATIONS, AND ANALYZING AL'S VALUE AFTER EACH STEP PROVIDES VALUABLE INSIGHTS.

STEP-BY-STEP ANALYSIS

INITIAL STATE

- INSTRUCTION: 'MOV AL, 0x95'
- HEXADECIMAL VALUE: '0x95'
- BINARY REPRESENTATION:

Hex	Binary	Decimal
0x95	1001 0101	149

- AL AFTER EXECUTION: '1001 0101'

THIS IS OUR STARTING POINT, AND THE VALUE OF AL IS 149 IN DECIMAL NOTATION.

AFTER THE 'SHL AL, 1' INSTRUCTION

- OPERATION: LOGICAL SHIFT AL LEFT BY 1 POSITION.
- EFFECT: EACH BIT SHIFTS LEFT BY ONE, AND THE LEAST SIGNIFICANT BIT (LSB) BECOMES 0. THE MOST SIGNIFICANT BIT (MSB) THAT IS SHIFTED OUT IS LOST.

- BINARY SHIFT:

BEFORE	1	0	0	1	0	1	0	1

SHIFTED	0	1	0	0	1	0	1	0

- RESULT:

HEX	BINARY	DECIMAL

0x2A	0010 1010	42

- AL AFTER EXECUTION: '0x2A' (DECIMAL 42)

ANALYSIS: THE LEFT SHIFT EFFECTIVELY MULTIPLIES THE ORIGINAL VALUE BY 2, PROVIDED NO BITS ARE LOST DUE TO OVERFLOW. IN THIS CASE, $149 \times 2 = 298$, BUT SINCE AL IS ONLY 8 BITS, OVERFLOW OCCURS, AND THE RESULT WRAPS AROUND, RESULTING IN 42 (WHICH IS $298 \bmod 256$).

AFTER THE 'ROR AL, 2' INSTRUCTION

- OPERATION: ROTATE AL RIGHT BY 2 BITS.
- EFFECT: THE BITS ARE ROTATED CIRCULARLY; THE BITS SHIFTED OUT ON THE RIGHT RE-ENTER ON THE LEFT.

- CURRENT AL: '0010 1010'

- ROTATION PROCESS:

- FIRST ROTATION (RIGHT BY 1):

BEFORE	0	0	1	0	1	0	1	0

AFTER	0	0	1	0	1	0	1	0

(SINCE ROTATION IS CIRCULAR, BITS MOVE FROM RIGHT TO LEFT)

- SECOND ROTATION (RIGHT BY 1):

- THE LAST BIT (0) MOVES TO THE FRONT.

- RESULT:

| BINARY | 0 0 1 0 1 0 1 0 --> ROTATE RIGHT BY 2:

- FIRST ROTATION: 0 0 1 0 1 0 1 0 → 0 0 1 0 1 0 1 0

- SECOND ROTATION: LAST TWO BITS '10' MOVE TO THE FRONT:

- BITS: '0 0 1 0 1 0 1 0'

- ROTATE RIGHT BY 2: BITS '10' (BINARY 2) MOVE TO FRONT:

- RESULT: '1 0 0 0 1 0 1 0'

- IN BINARY: '1 0 0 1 0 1 0 1'

- HEXADECIMAL: '0x95'

- AL AFTER EXECUTION: '0x95' (DECIMAL 149)

ANALYSIS: THE ROTATE OPERATION RESTORES THE ORIGINAL VALUE BECAUSE ROTATING RIGHT BY 2 ON '0010 1010' LEADS BACK TO '1001 0101', WHICH IS 0x95. CIRCULAR ROTATIONS ARE USEFUL IN CRYPTOGRAPHY AND CHECKSUM ALGORITHMS DUE TO THEIR REVERSIBLE NATURE.

AFTER THE 'SHR AL, 3' INSTRUCTION

- OPERATION: SHIFT AL RIGHT LOGICALLY BY 3 BITS.

- EFFECT: BITS ARE SHIFTED RIGHT, WITH ZEROS FILLING IN THE VACATED MSB POSITIONS.

- CURRENT AL: '1001 0101' (0x95)

- BINARY SHIFT:

BEFORE	1 0 0 1 0 1 0 1
AFTER	0 0 0 1 0 0 1 0

- RESULT:

HEX	BINARY	DECIMAL
0x12	0001 0010	18

- AL AFTER EXECUTION: '0x12' (DECIMAL 18)

ANALYSIS: THE LOGICAL RIGHT SHIFT REDUCES THE VALUE APPROXIMATELY BY A FACTOR OF 8, AS IT DISCARDS THREE BITS ON THE RIGHT. NOTABLY, ZEROS FILL THE LEFTMOST BITS, WHICH IS IMPORTANT WHEN DEALING WITH UNSIGNED DATA.

AFTER THE 'ROL AL, 4' INSTRUCTION

- OPERATION: ROTATE AL LEFT BY 4 BITS.

- EFFECT: THE BITS ARE ROTATED CIRCULARLY; THE 4 MOST SIGNIFICANT BITS MOVE TO THE RIGHT END, AND THE 4 LEAST SIGNIFICANT BITS MOVE TO THE LEFT.

- CURRENT AL: '0001 0010'

- BINARY ROTATION:

- ROTATE LEFT BY 4:

- UPPER 4 BITS: '0001'

- LOWER 4 BITS: '0010'

- ROTATED VALUE:

- THE LOWER 4 BITS '0010' MOVE TO THE UPPER 4 BITS, AND THE UPPER 4 BITS '0001' MOVE TO THE LOWER 4 BITS.

- RESULT: '0010 0001'

- HEXADECIMAL:


```
| BINARY | HEX |  
|-----|-----|  
| 0010 0001 | 0x21 |
```

- AL AFTER EXECUTION: '0x21' (DECIMAL 33)

ANALYSIS: ROTATING LEFT BY 4 BITS EFFECTIVELY SWAPS THE UPPER AND LOWER NIBBLES (4-BIT GROUPS). THIS OPERATION IS OFTEN USED IN NIBBLE SWAPPING, DATA ENCODING, OR CRYPTOGRAPHIC TRANSFORMATIONS.

SUMMARIZED FINAL REGISTER STATES

STEP	INSTRUCTION	AL VALUE (Hex)	DECIMAL	BINARY REPRESENTATION	NOTES
1	MOV AL, 0x95	0x95	149	1001 0101	INITIAL VALUE
2	SHL AL, 1	0x2A	42	0010 1010	MULTIPLIED BY 2 WITH OVERFLOW WRAP
3	ROR AL, 2	0x95	149	1001 0101	CIRCULAR ROTATION RESTORES INITIAL VALUE
4	SHR AL, 3	0x12	18	0001 0010	DISCARDS LOWER BITS, SHIFTS RIGHT
5	ROL AL, 4	0x21	33	0010 0001	NIBBLE SWAP VIA

In The Following Code Sequence Show The Value Of Al After Each Shift Or Rotate Instruction Has Executed

In The Following Code Sequence Show The Value Of Al After Each Shift Or Rotate Instruction Has Executed

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